Hfss Antenna Design Kit

system sensitivity to parameter variation will be determined by simulating a wide range of 3-d field solver inputs for each antenna parameter. the range of the parametric variation is defined as the difference between the high and low extremes of the parametric variation. the high parameter value is taken as the true parameter with the software taking the low parameter value as the default. for example, in figure 10 the range of the variation for the short circuit capacitance is from 0.002 to 0.012. the range of the variation of the capacitance will be greater in an amp's hot and cold temperature operating point (see figure 15). for each range of variation, the 3-d field solver is used to generate the 3-d solution and results in a set of 3-d field solver inputs. the initial solution is generated at the first range of variation. the 3-d field solver is then called again with the next range of variation. the 3-d solution is generated and the differences are reported. this process is repeated for each range of variation to form a series of 3-d solutions. note that in the circuit simulation, the amplifier and phase shifter paths do not represent rf power delivery and are purely for verification purposes. an ideal power amplifier is used for this purpose. the dc gain of the ideal power amplifier is obtained from a previous ac characterization of the amplifier (figure 9) whereas the phase is set using the parameters from the network design. phase variation is seen as a shift of the return loss curve of the amplifier. as both the phase and amplitude weights are designed for optimal rf performance, they directly map onto the rf performance of the system. any tradeoffs affecting the performance of the rf system are reflected in the channel parameter grid plots. as shown in figure 10, in the case of the channel parameter grid, optimal performance is obtained around 0.5 for the range of the parameters considered.

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as the system specifications are met for rf performance, the dynamic link feature within designer is used to check the electrical parameters of the design and verify their values are within the expected range. the rf current, voltage and power parameters of the component are set to those required in the circuit design and the individual path parameters are set to the respective values from the network design. the component is then simulated and the resulting power levels and impedance of the individual paths are compared to the desired values. this process is used to find any inconsistencies in the values of the path parameters or power levels of individual paths, if the error is less than a percentage defined by the designer. the 2x2 multi-beam antenna array is designed for the specific transceivers used for the system and the design process starts with the construction of the basis plan which provides a schematic of the antenna array. the detailed modeling starts with the creation of the patch antenna array in hfss. first, the transmitter and receiver are modeled to establish all the necessary rf paths and power levels from the circuit topology. a detailed rf network plan is generated for the antenna array from the circuit simulation results. once the network plan is complete, the patch antenna array geometry is designed to achieve a best coverage for the specified frequency in the hfss multi-beam project. the previous stage of planning permits computation of optimal array parameters for each beam. the array parameters are then transferred to the patch antenna array model and final geometry is optimized to obtain the operating frequency of the antenna array. figure 6 shows the array as defined by hfss design under the constraint of a perfectly balanced radiating structure to avoid any unwanted modes. an optimized antenna array covering the targeted frequency bands is shown in figure 7.

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